Abstracts

Silicon monolithic balanced oscillators using on-chip suspended active resonators

Y. Sun, J.L. Tauritz and R.G.F. Baets. "Silicon monolithic balanced oscillators using on-chip suspended active resonators." 1998 Radio Frequency Integrated Circuits (RFIC) Symposium 98. (1998 [RFIC]): 149-152.

Silicon microwave monolithic balanced oscillators using suspended active resonators realized in a f/sub T/=15 GHz BJT process are presented. These micro-machined oscillators feature lower power consumption and lower phase noise (/spl sim/5 dBc lower). Suspended active resonators, tuned to self-oscillate and having an even simpler topology, are also demonstrated.

❑ Return to main document.